



**MAIL STOP AF  
RESPONSE UNDER 37 C.F.R. § 1.116  
EXPEDITED PROCEDURE  
EXAMINING GROUP 3711**

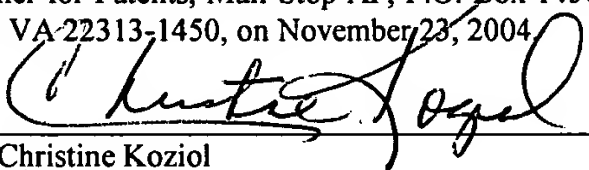
**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application No. : 09/981,603  
Applicants : Robert F. Dvorak  
Kon B. Wong  
Filed : October 17, 2001  
Title : Arc Fault Circuit Interrupter System  
  
TC/A.U. : 2836  
Examiner : Boris Benenson  
  
Docket No. : 47181-00259  
Customer No. : 23569

Commissioner for Patents  
Mail Stop AF  
P.O. Box 1450  
Alexandria, VA 22313-1450

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postage prepaid, in an envelope addressed to the Commissioner for Patents, Mail Stop AF, P.O. Box 1450, Alexandria, VA 22313-1450, on November 23, 2004.

Signature: 

Christine Koziol

**AMENDMENT TRANSMITTAL**

Dear Sir:

Transmitted herewith is an "Reply After Final" for this application.

The Applicants are other than a small entity.

The proceedings herein are for a patent application, and the provisions of 37 C.F.R. § 1.136 apply.

The Applicants believe that no extension of time is required. This conditional petition is being made, however, to provide for the possibility that the Applicants have inadvertently overlooked the need for a petition for extension of time.

The fee for claims (37 C.F.R. § 1.16(b)-(d)) has been calculated as shown below.

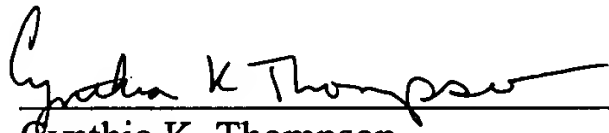
	Claims Remaining	Highest No. Paid For	Extra	Small Entity	Large Entity
Total	46	46	0 x	\$ 9 = \$	\$ 18 = \$ 0
Independent	7	7	0 x	\$ 43 = \$	\$ 86 = \$ 0
Multiple Dependent Claim Presented				\$ 145 = \$	\$ 290 = \$ 0
TOTAL ADDITIONAL FEE					\$ 0

There is no additional fee for claims.

The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment, to Deposit Account No. 10-0447 (47181-00259). A duplicate copy of this Transmittal is enclosed for that purpose.

Respectfully submitted,

Date: November 23, 2004

  
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AF/2836  
IFW  
BOX AF  
RESPONSE UNDER 37 C.F.R. § 1.116  
EXPEDITED PROCEDURE  
EXAMINING GROUP 3712

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No. : 09/981,603  
Applicants : Robert F. Dvorak  
Kon B. Wong  
Filed : October 17, 2001  
Title : Arc Fault Circuit Interrupter System  
  
TC/A.U. : 2836  
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Signature:

Christine Koziol

**REPLY AFTER FINAL**

Dear Sir:

This is in response to the Final Office Action dated September 29, 2004. The shortened statutory period for response is three months from the mailing date, i.e., by December 29, 2004, and this response is being filed within that time period. Please enter the following remarks into the record for this application.

Remarks/Arguments begin on page 2 of this paper.